

FIG. 1

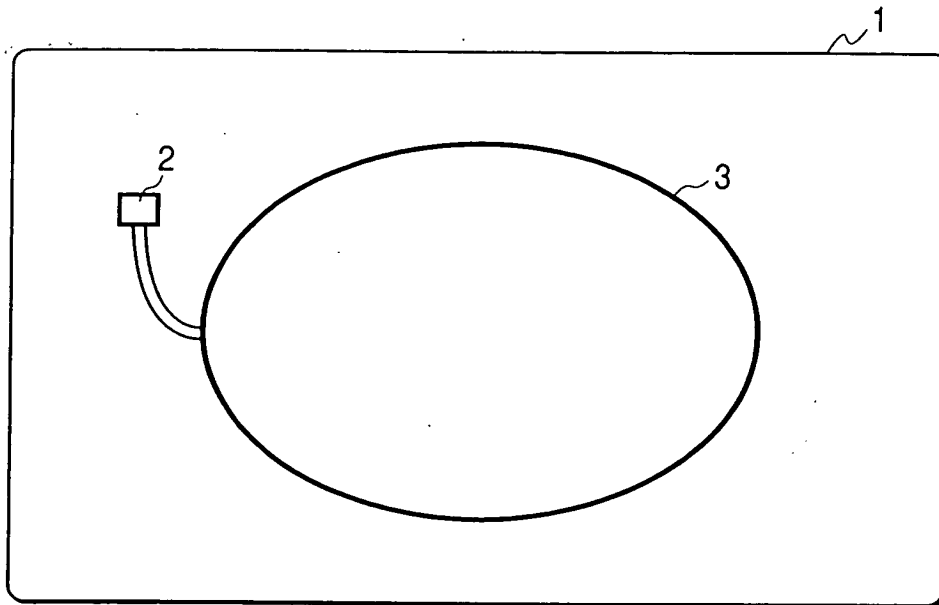


FIG. 3

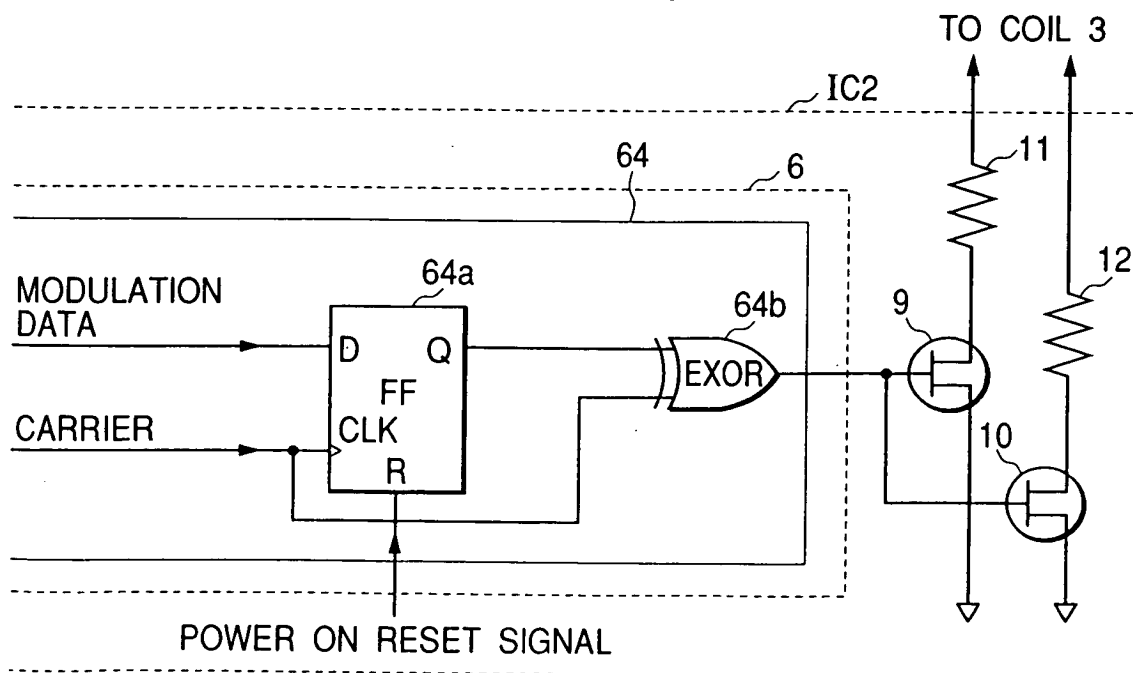


FIG. 2

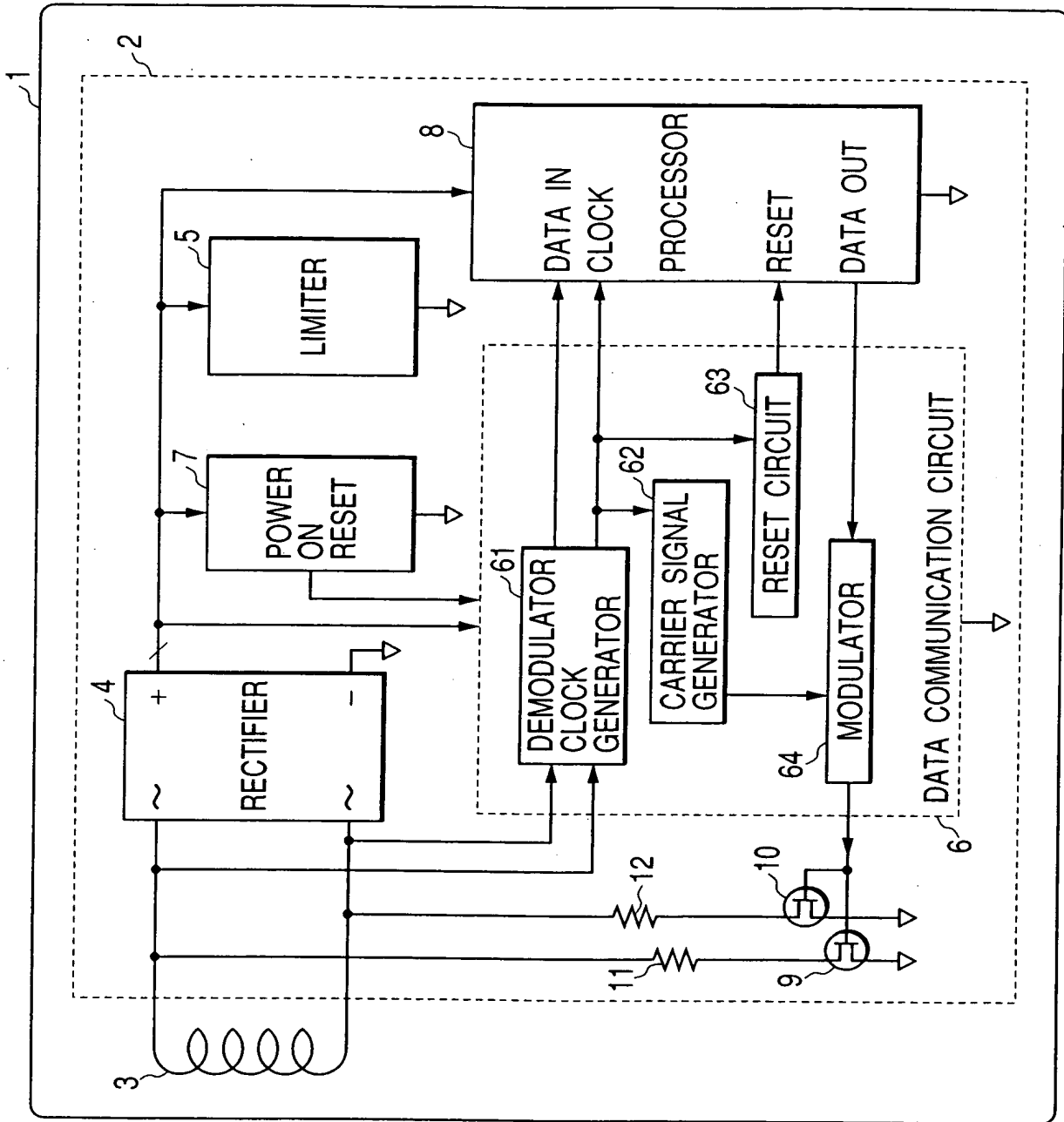
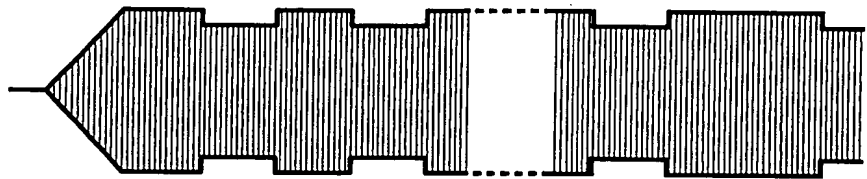
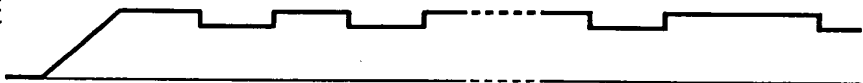


FIG. 4

INDUCED  
VOLTAGE  
WAVEFORM  
AT COIL 3



OUTPUT VOLTAGE  
OF RECTIFIER



R INPUT SIGNAL  
OF FF64a



CLK INPUT SIGNAL  
OF FF64a



D INPUT SIGNAL  
OF FF64a



Q OUTPUT SIGNAL  
OF FF64a



OUTPUT SIGNAL  
OF EXOR64b



SWITCHING  
BEHAVIOR OF  
FET9, 10

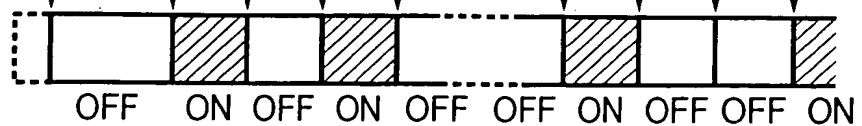


FIG. 5

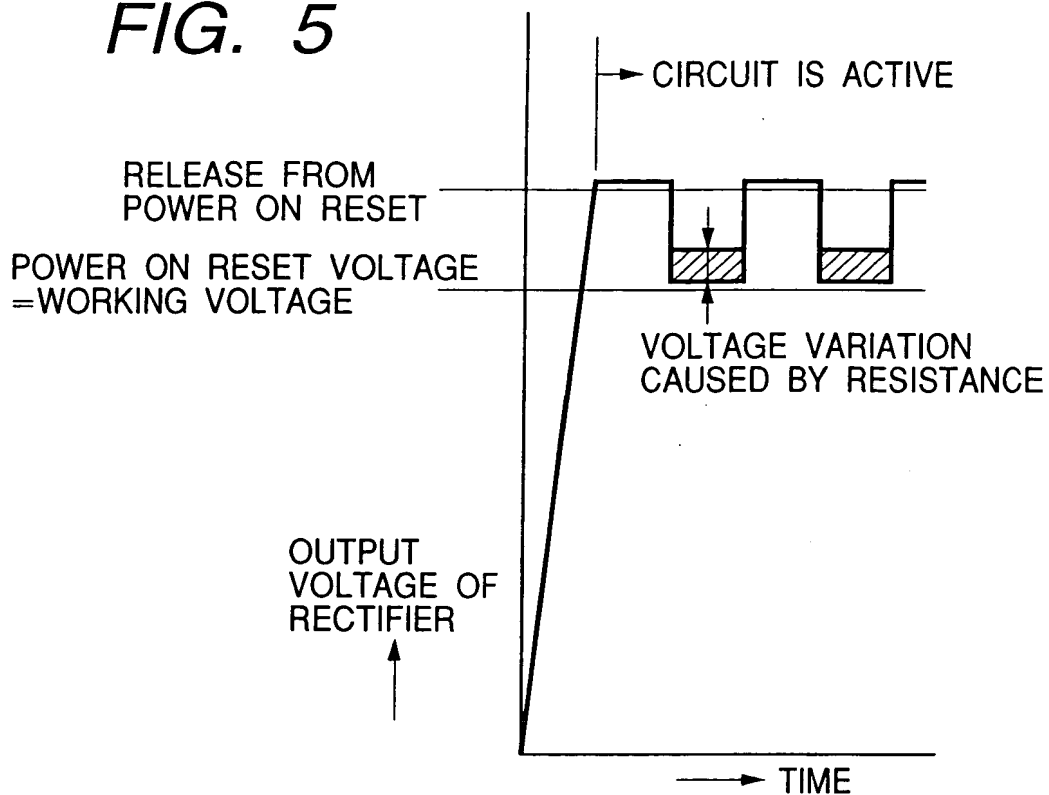


FIG. 6

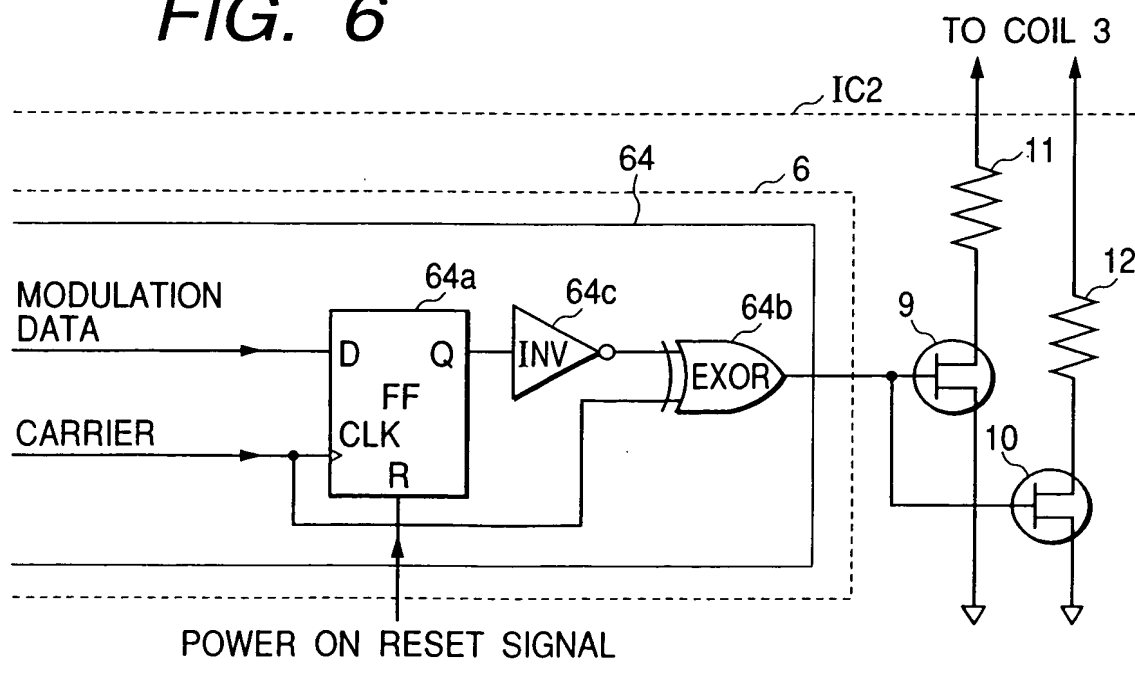


FIG. 7

INDUCED  
VOLTAGE  
WAVEFORM  
AT COIL 3

OUTPUT VOLTAGE  
OF RECTIFIER

R INPUT SIGNAL  
OF FF64a

CLK INPUT SIGNAL  
OF FF64a

D INPUT SIGNAL  
OF FF64a

OUTPUT SIGNAL  
OF INV64c

OUTPUT SIGNAL  
OF EXOR64b

SWITCHING  
BEHAVIOR OF  
FET9, 10

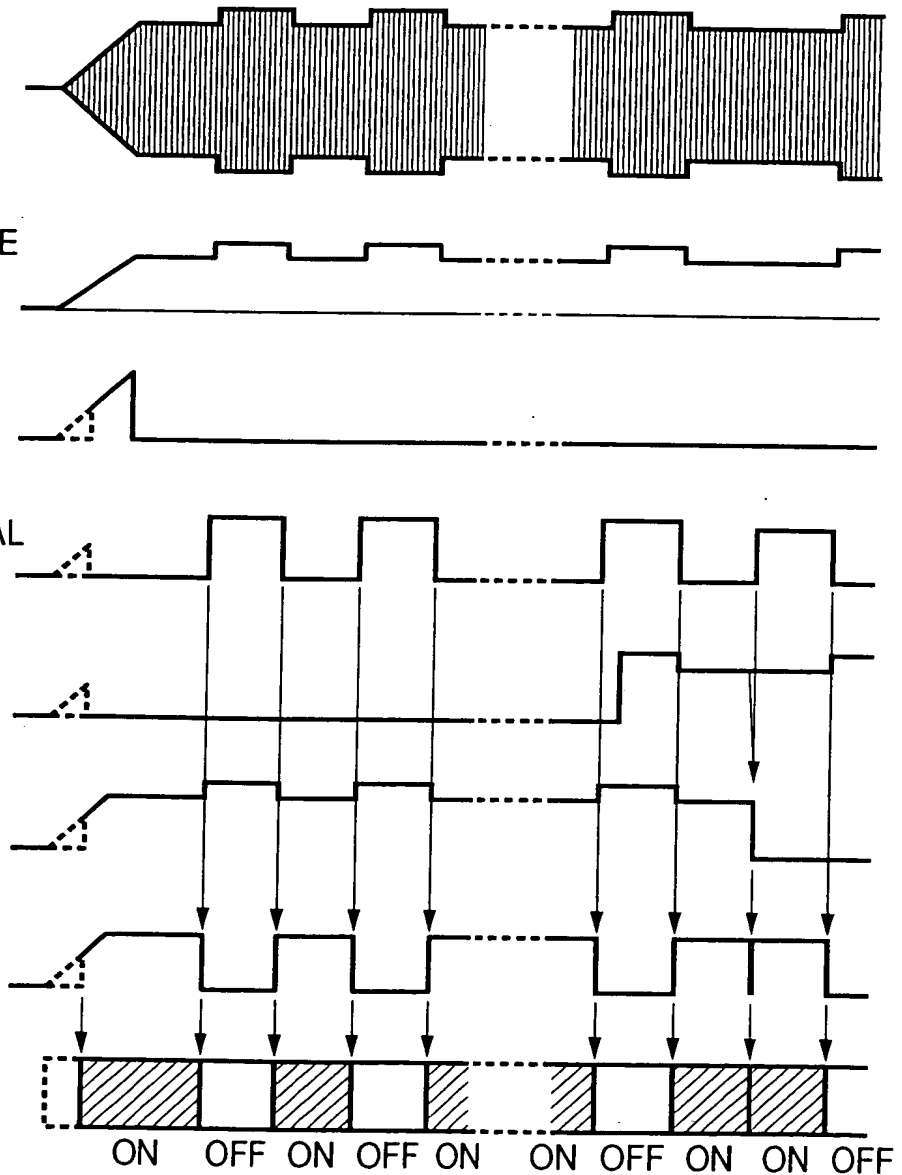


FIG. 8

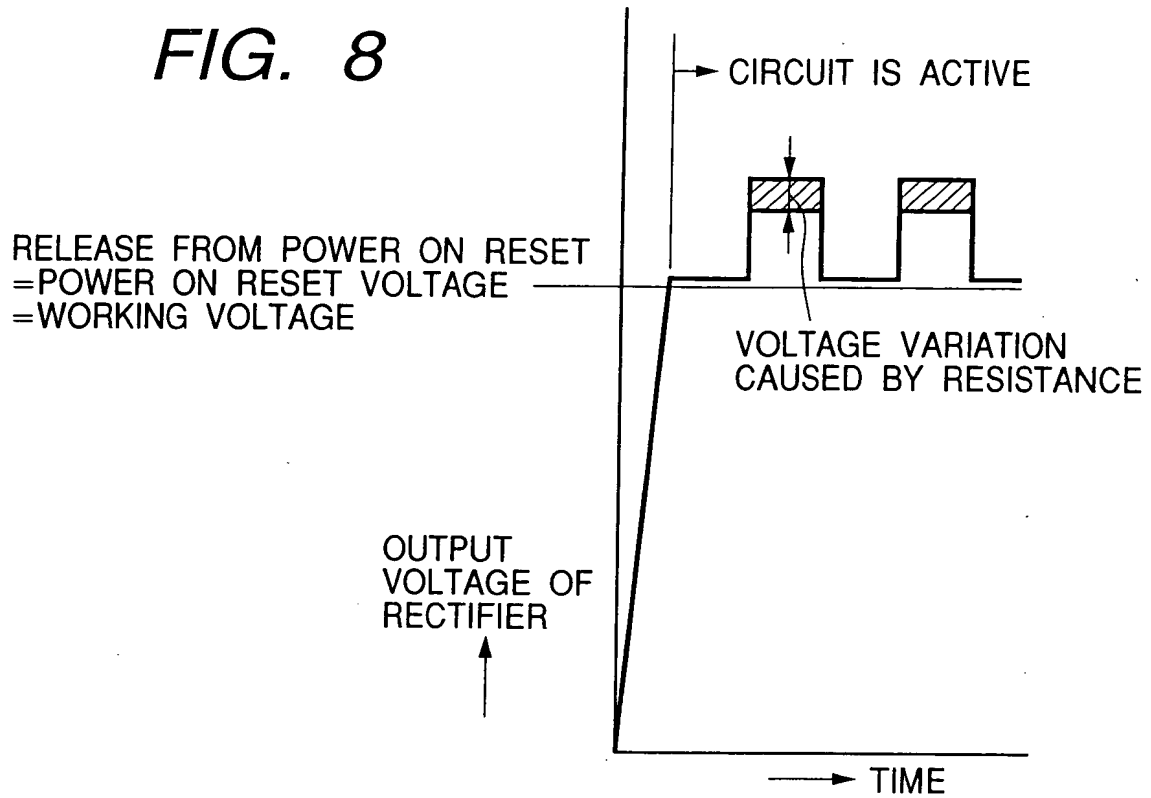


FIG. 9

